



1980, JUNE 24
**CW TELELEADER
 CWR-682A**
 SCHEMATIC
 DIAGRAM

(CWR-682 P.C.B. 780919-1)

ACC OUTPUT CODE WITH 8-BIT, JIS CODE + STROBE

- COMPUTER BLOCK**
- * MPU MC6802 (HITACHI HD46802P)
 - * VIDEO RAM 256 LETTERS
 - * CHARACTER GENERATOR MCM66734P
 - * EPROM C2708 (CTR-V4A)

POWER SUPPLY

DO-D6 WITH ASCII CODE
 COMPATIBLE
 (NEGATIVE LOGIC OUTPUT)

Telereader CWR-682A Halbleiter

(Alle ROT markierten ICs sind im Computer Block)

1A	2708	EPROM
1C	MC6802	MPU
1E	HA17741	OP AMP
2D	74LS20	2× QUAD NAND
2F	TA7157	PLL
3A	MB471 (P8212)	OUTPUT DATA LATCH
3C	MCM66734P	CHARACTER GENERATOR
3D	74LS27	3× TRIPLE NOR
3E	74LS10	3× TRIPLE NAND
4A	74165	8 BIT SHIFT REGISTER
4B	CD4030	4× EX-OR
4C	D5101LC	STATIC RAM
4D	74LS125AP	4× 3-STATE BUFFER
4E	74LS125AP	4× 3-STATE BUFFER
4F	TC4069	6× INVERTER
5B	MSM4011	4× DUAL NAND
5C	D5101LC	STATIC RAM
5D	74LS125	4× 3-STATE BUFFER
5E	TC4069	6× INVERTER
6B	74LS93	DECADE COUNTER
6C	74LS157	MULTIPLEXER
6D	74LS157	MULTIPLEXER
6E	HD14027	2× JK FLIP FLOP
6F	74LS08	4× DUAL AND
7B	74LS89	64 BIT RAM
7C	74LS89	64 BIT RAM
7D	74LS30	8 INPUT NAND
7E	MSM4011	4× DUAL NAND
7F	HD14081	4× DUAL AND
8B	74LS93	DECADE COUNTER
8C	74LS93	DECADE COUNTER
8D	74LS04	6× INVERTER
8E	TC4069	6× INVERTER
8F	HD14027	2× JK FLIP FLOP
D10	M160 ?	
D11		
D12	1S1885	
D13	1S1885	
D14	1S1885	
D15	1S1885	
D16	1S1885	
TR3	2SC945	
TR4	2SC1515	
TR5	2SC458	